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Designing a Media of Active Learning for Teaching the Passive High Pass Filter (PHPF) by developing its Bode Plot by using Excel

Humuntal Rumapea¹ Dahlan Sitompul² Darwis Robinson Manalu³

¹ Universitas Methodist Indonesia.

² Department of Teknik Informatika, ATI-Immanuel Medan, and Program Studi SI Ilmu Komputer, Fakultas Ilmu Komputer <mark>dan</mark> Teknologi Informasi, Universitas Sumatera Utara, Medan, Indonesia.

³Universitas Methodist Indonesia.

Abstract

In this paper, the writer developed an active learning media that ass that has implemented an active learning used in a strategy in the class, and especially for the developing nation student to help them in addressing their hindrances in mastering the technology due to the lack of budget and facility to run the conventional way of teaching and learning, especially in this era of Covid 19 Pandemic that has forced us to do virtual learning to inhibit the widespread of the deadly virus; this media will be helpful. This media will help the teacher to develop his/her teaching planning and strategy and help the student in understanding the attributes or the properties of the Passive High Pass Filter (PHPF) easily by observing the Excel table of the PHPF variable and the Bode Plot of the PHPF instead of calculating each of the statiable values of the PHPF manually; the student can not observe and see the big picture of the PHPF characteristics promptly and will not enable them to understand it immediately. This media was developed by using two well-known application//programs; Excel and Intelligent Schematic Input System-ISIS Proteus. The Excel shows the Bode Plot of the PHPF by using the table variables values of the PHPF (the frequency of the input, the Gain of the PHPF in Decibel-dB, and the Phase Angle between the output voltage (Vo) of the circuits and the input voltage (Vin) put into the PHPF input port) and the ISIS Proteus will be used to prove the values shown on the excel table and the Bode Plot shown by excel; ISIS Proteus is a specially purposed simulator to simulate Electrical, Electronics, Computer, Microprocessor, Microcontroller, and control devices working principle and their properties. The developed media of teaching has successfully shown the attributes of PHPF.

Keywords: Active Learning; High Pass Filter; Bode Plot; Excel; Proteus ISIS; Media of Teaching

INTRODUCTION

The filter is one of the fundamental elements of signal processing and is widely used in many electronic devices. Based on the components used in the designing of the filter, the filter can be classified into two types, Active Filter (the use of active components such as OP-Amp, and Transistor beside the filter main components it's self such as Resistor, Capacitor, and Inductor), and Passive Filter (the used of resistor, Capacitor, and Inductor in its design). As mentioned before in the Active Filter, the Operational Amplifier (the Op-Amp) is used to amplify the output signal of a filter to be processed further. Based on the type of the required signal frequency to be passed to the output of the filter, the filter can be divided into two types of filters as follows, the High Pass Filter, and the Low Pass Filter. A combination of the two filters mentioned above (HPF and LPF) result in the third and the fourth types of filter can be built; the Band Pass Filter-BPF, and Band Stop Filter-BSF. To make the output of the filter close enough to the output of an ideal filter, a high-order type of filter can be used. The higher the order of the filter (by cascading the first order of the filter to form the higher order of the filter) the better the output of the filter, but as the consequence, the math equation of the filter will be more complex and of course it will be more expensive. In this paper, we will discuss only the first type of filter, the Passive High Pass Filter (PHPF); First Order High Pass Filter. Some engineers and scientists call the filter a transfer function and are widely mentioned in Control System Engineering. Understanding the characteristics of a filter is a prerequisite for an Engineer to design complex electronic devices. Due to the importance of understanding how the filter works and as an effort to help the student in answering the worksheet on signal filter provided by some organization[1], the author develops an active learning media in teaching this subject. This application will enable the teacher to run the class conveniently and the student can understand the subject easily and can use it to design their own first order PHPF conveniently.

RELATED WORK

Many scholars and researchers have developed a lot of kinds of media of active learning and teaching by using computer programming or not in the form of simulation. Some of the published papers in this field are as follows, in this paper [2] the author reported the design of a decoder to display the result of the digital voltage level on a seven-segment display in the form of characters "H" and "L". The author proposed the use of his design is implemented in active learning classes especially in the electronic digital laboratory. In paper [3] the authors develop an active learning simulator using both MCU8051 IDE and (Small Devices C Compiler-SDCC). In this paper, the author reported that the simulator will display the level voltage of a pin of an 8051 port on a virtual seven-segment display and propose to use it as a media of teaching especially in the classes that have adopted active learning strategies. In this paper [4] the authors reported the development of a simulator program that simulates the process of multiple interrupts runs on an 8051 microcontroller by using MCU8051 IDE and ASEM 51 [4]. In this paper [5] the author developed a media of active learning and teaching in the microcontroller field and programming it in C language (SDCC) by using struct and pointer in retrieving a character stored in IRAM of the microcontroller to be displayed on a virtual LED. In this paper [6] the author developed a teaching media for ALFHIE class, that simulates the interfacing of MCS-51 to a 4 X 4 Keypad and displays the character input from the Keypad onto two different displays (LED and LCD), the program run on ASM-51. In this paper [7] the author develops a simulation for ALFHIE class. This simulation program was developed by using Small Devices C Compiler SDCC) to simulate how to read the Busy Flag of the LCD and proposed it rather than using a delay time that is hard to calculate it and each and every LCD has its own delay time.

Proposed System

In this paper, the author proposed the development of an active learning media of the First Order Passive High Pass Filter. The media is in the form of a simulator and was developed by using two famous application programs Excel and ISIS Proteus. It can be used by the teacher or lecturer to teach students of the Filter characteristic or attributes and the student can use it by themselves and change the values of the filter variables (frequency of the input signal, the Capacitor, and Resistor variables). As mentioned above the Excel will display the Bode plot of the filter based on the value of the filter variables inou namely Signal frequency, Resistor, and Capacitor values to the excel table. The teacher can ask the student to observe the Bode Plot of the filter and compare the Bode Plot's result to the theory taught by the teacher or the lecturer. The teaching participants can also compare the result shown on the Bode Plot to the result obtained by the ISIS Proteus. The output of the Excel (table and Bode Plot) and the ISIS Proteus (Simulation of a particular output gain and Phase Angle between the output voltage and the input voltage of a specific value of the Filter Frequency)[8][9][10][11]. After running the simulation student can ponder in their heart and mind the three results obtained from the theory given by the lecturer or the teacher, from the Bode Plot shown by the Excel, and from the result obtained by the ISIS Proteus as well; they should be in accord.

Simulation

In this simulation, the author simulated a PHPF frequency response with Resistor values and Capacitor values 20 K Ω , 1.5nF respectively; the student can change these two variables in their future simulation and analysis as their wish. The cutoff frequencies, Gain in dB, and the Phase angle, 5305 Hz, -3dB (0.707), 45 degrees respectively[12]. Figure 1 below shows the diagram of the PHPF

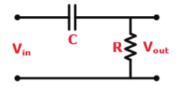


Fig. 1: The Diagram of PHPF[13]

It can be seen from the diagram that the output voltage is measured across the resistor. We can apply the voltage divider equation to find the output voltage[14]. The equation for the gain of a PHPF is as follows[12][15],

$$G = \frac{R}{(R^2 + (\frac{1}{(\mu)C})^{-2})^{-1/2}}$$

The cutoff frequency for the PHPF is

$$Fc = 1/(2\pi RC)$$
 [12][15]

And the gain of the PHPF at the cutoff frequency is 0.707 $(1/((2)^{1/2}) \text{ or } -3 \text{ dB}[15][12])$

The phase shift equation for PHPFF [12] is

Phase Shift $(\theta) = arctan(1/\omega)CR)$

By using the mentioned above equations Gain in dB (20 log Vo/Vin) [15], Fc, and Phase Shift (θ) and putting the values of the PIPEr components (R and C) and the variable frequency of the input signal into the Excel as shown in figure 1 below, we can observe the result of the simulation; the result of the simulation has been captured and shown in Fig. 2, Fig. 3, and Fig. 4. Fig. 2 shows the table of PHPF variables values, the input signal frequency, the gain in dB, and the Phase Angle inofread degree. Fig. 3 shows the Bode Plot of the filter as the result of

4300 4,017729581

50,97416461 0,629671

the simulation. Fig. 4 (Fig. 4.1-Fig. 4.4) shows the result of the ISIS Proteus Simulation.

SIS P	roteus	Simulation.		
lz)		-10	PhaseAngle in	110/00/0
		dB -	Degree	H(V0/Vin)
	10	54,49399297	89,89200013	0,001885
	15	50,97218708	89,83800043	0,002827
	20	48,47343935	89,78400102	0,00377
	25	46,53527381	89,730002	0,004712
	30	44,95169132	89,67600345	0,005655
	35 40	43,61280567 -42,4530246	89,62200548 89,56800819	0,006597 0,00754
	45	41,43003972	89,51401166	0.008482
	50	-40,5149632	89,46001599	0,009424
	60	-38,931508	89,35202763	0,011309
	70	- 37,59277278	89,24404387	0,013194
	80	- 36,43316525	89,13606548	0,015078
	90	35,41037706	89,02809323	0,016962
	100	34,49552034	88,92012788	0,018846
		-		
	110	33,66799055	88,8121702	0,02073
	120 130	32,91257407 -32,2177175	88,70422096 88,59628092	0,022614 0,024497
	140	-31,5744402	88,48835084	0,02638
	150	30,97562289	88,38043149	0,028263
	160	- 30,41552636	88,27252363	0.030146
	170	-29,8894563	88,16462804	0,032028
		-		
0	29,39	352412	88,05674546	0,03391
)	28,92	447244	87,94887666	0,035791
)	28.47	954554	87,84102241	0,037672
		-		
)	24,96	541795	86,76344696	0,056458
)	20,55	298396	84,61590408	0,093832
)	14.64	- 560681	79,32525059	0,185234
0		050375	69,34400262	0,352756
	0,00	-	55,5 1400202	5,552150
)	6,156	552019	60,51241904	0,492235
)	4,407	593076	52,98435543	0,602033
	4,272	- 080978	52,3020101	0,611499
00		-	-	
00	4,142	224867	51,6319975	0,62071

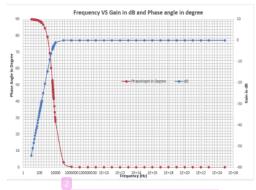


Fig. 3: The Bode Plot of PHPF[11]

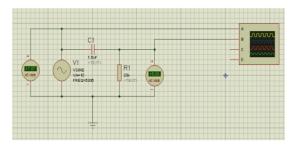


Fig. 4.1: The result of the ISIS Proteus Simulation; the PHPF circuits.

From Fig. 4.1 it can be seen clearly that for an input of a sinusoidal signal with an amplitude of 10 V (AC), a 7.07 V (AC) voltage will be read by the AC voltmeter, $\Delta V_{AC} \equiv$ Error V_P/(2^0.5) and the output voltage at the Cutoff Frequency will be read as a 5.03 V; 0.707 x Vin=0.707 X 7.07 V.

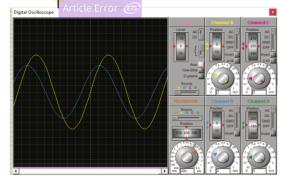


Fig. 4.2: The result of the ISIS Proteus Simulation; The Phase Angle at the Cutoff Frequency

Fig. 4.2 shows the Phase Angle of 45° ; the input signal lagging the output signal (the output signal leading the input signal) by 45° at the cutoff frequency; at 5305 Hz (5.305 KHz)[16][17].

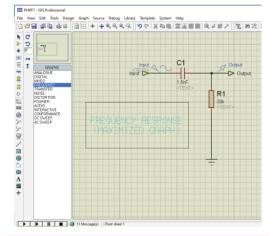


Fig. 4.3: The Citcuit simulation of PHPF to obtain the Bode Plot of Fig. 4[18]

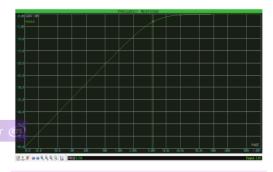
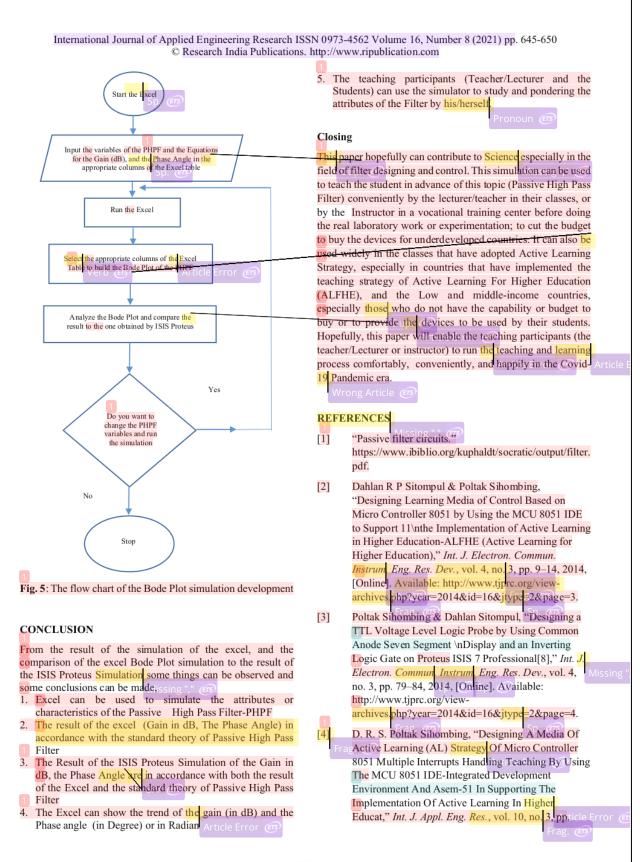


Fig. 4.4: The result of the ISIS Proteus Simulation; Bode Plot obtained from the ISIS Proteus simulation[18]

From Fig. 4.4 we can see that the cutoff frequency of the PHPF is 5.3 KHz; the gain is -3 dB.

The Flow Chart of the program

The flow chart below shows the taken steps to input the variables of the PHPF into the Excel table and run it and compare the obtained result to the obtained result by ISIS Proteus; the Gain (in dB), Phase Angle in degree, and the cutoff frequency in Hz [19] [20][21].



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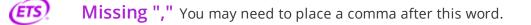


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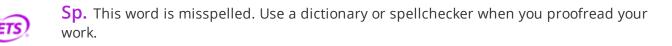
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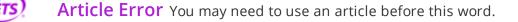
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